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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/708,238	02/18/2004	Hiroyuki YAMAUCHI	28569.5817	2237
20322 7	590 06/30/2005		EXAM	INER
SNELL & WILMER			CHO, JAMES HYONCHOL	
ONE ARIZONA CENTER 400 EAST VAN BUREN PHOENIX, AZ 850040001			ART UNIT	PAPER NUMBER
			2819	

DATE MAILED: 06/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

VA 2

	Application No.	Applicant(s)			
Office Action Summers	10/708,238	YAMAUCHI ET AL.			
Office Action Summary	Examiner	Art Unit			
	James Cho	2819			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	is (a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1)⊠ Responsive to communication(s) filed on 18 Fe	ebruary 2004.				
· _ · ·	· · · · · · · · · · · · · · · · · · ·				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4)⊠ Claim(s) <u>1-23</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.					
5)⊠ Claim(s) <u>21-23</u> is/are allowed.					
6)⊠ Claim(s) <u>1-5,8-14,16-18 and 20</u> is/are rejected.					
7) Claim(s) <u>6,7,15 and 19</u> is/are objected to.					
8) Claim(s) are subject to restriction and/or	election requirement.				
Application Papers					
9) The specification is objected to by the Examiner	•				
10)⊠ The drawing(s) filed on <u>18 February 2004</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.					
Applicant may not request that any objection to the o	Irawing(s) be held in abeyance. See	e 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correcti		` '			
11)∐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No. 09/553,308.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> </ul>					
* See the attached detailed Office action for a list of the certified copies not received.					
Attachment(s)					
Notice of References Cited (PTO-892)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Interview Summary (PTO-413)  Paper No(s)/Mail Date					
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 2/04, 4/04.		atent Application (PTO-152)			

Application/Control Number: 10/708,238

Art Unit: 2819

#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-3, 4-5, 8-14, 16-18, and 20 are rejected under 35 U.S.C. 102(b) as being anticipated by Beers et al. (US PAT No. 5,568,064).

Regarding claims 1, 11 and 16, Fig. 2 of Beers et al. teaches a signal transmitting/receiving apparatus, comprising: a transmitting device (20 on the left) for transmitting a first data and a second data (data over 22 and 24); a receiving device (20 on the right) for receiving the first data and the second data; a data line (22 and 24) for transmitting the first data and the second data; wherein the transmitting device and the receiving device are connected to each other through the data line, the transmitting device comprising: a driver circuit (12) for outputting the first data to the data line; and a circuit (10) for outputting the second data to the data line, the receiving device comprising: a terminating resistor (14, active termination) connected to the data line; a receiver circuit (16) for detecting the first data from the data line; and a bias generating means (10) for generating a bias voltage applied to the terminating resistor; the bias generating means setting the bias voltage based on the second data from the data line (col. 5, lines 29-30).

Application/Control Number: 10/708,238

Art Unit: 2819

Regarding claims 2, 12 and 17, Fig. 2 of Beers et al. teaches a signal transmitting/receiving apparatus according to claims 1, 11, and 16 wherein the bias generating means comprises a bias generating circuit (40 in Fig. 3) and a reference voltage generating circuit (38 in Fig. 3).

Regarding claims 3, 13 and 18, Fig. 2 of Beers et al. teaches a signal transmitting/receiving apparatus according to claims 1, 11, and 16, wherein the data line for transmitting the first data and the data line for transmitting the second data are different (22 and 24 are different).

Regarding claim 4, Fig. 2 of Beers et al. teaches a signal transmitting/receiving apparatus according to claim 1, wherein the data line comprises a pair of differential lines (24 has two lines differential).

Regarding claim 5, Fig. 2 of Beers et al. teaches signal transmitting/receiving apparatus according to claim 3, wherein the data line comprises a pair of differential lines (24 has two lines differential).

Regarding claims 8, 14, and 20, Fig. 2 of Beers et al. teaches signal transmitting/receiving apparatus according to claims 1, 11, and 16, further comprising a ground interconnect line for connecting a ground of the transmitting device and a ground of the receiving device (ground are connected together in Fig. 2).

Regarding claim 9, Fig. 2 of Beers et al. teaches 9 a signal transmitting/receiving apparatus according to claim 1, wherein the data line has flexibility (22 and 24 are inherently flexible over temperatures).

Regarding claim 10, Fig. 2 of Beers et al. teaches a signal transmitting/receiving apparatus according to claim 8, wherein the ground interconnect line has flexibility (22 and 24 are inherently flexible over temperatures).

### Allowable Subject Matter

Claims 21-23 are allowable over the prior art of record.

Claims 6-7, 15 and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: Although Beers et al. bidirectional transmission line driver/receiver, one of ordinary skill in the art would not have been motivated to modify the teaching of Beer et al. to further includes, among other things, the specific of the termination resistor being connected to short circuit between the pair of differential lines as set forth in the claims 6-7, 15 and 19, and a plurality of terminating resistors connected to the plurality of corresponding data lines, respectively; and a plurality of receiver circuits for detecting the plurality of first data from the plurality of data lines, respectively, at least one bias generating means for generating a bias voltage to be applied to the plurality of

Art Unit: 2819

terminating resistors, the at least one bias generating means setting the bias voltage based on the at least one second data from the plurality of data lines as set forth in claim 21.

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Okayasu (US PAT No. 6,114,898) discloses an output driver with bias generator.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Application/Control Number: 10/708,238

Art Unit: 2819

James H. Cho Primary Examiner Art Unit 2819

June 27, 2005